

**Faculty Development Programme (FDP) ON Advanced Compiler Design
Appendix I**

Tentative Schedule

(2nd June 2017 to 11th June 2017)

Day& Date	9.00 – 11.00		11.15 – 13.15	14.00 – 16.00	16.15 – 18.15/18.45
02.06.2017 Friday	Registration From 8.30 am	Inaugural Address At 9.30am	Overview of Compilers (RGR)	Machine Learning Techniques in Optimizing Compilers (SP)	GNU C Compiler Optimizations Overview (RU)
03.06.2017 Saturday	Introduction to Compilers (TR)		Automata and Grammars (TR)	Lexical Analysis (TR)	Tutorial on CFGs (USNR)
04.06.2017 Sunday	Project Problem Description - Subset C Specifications and Grammar (TR)		Introduction to Lex, Yacc Tools (USNR)	Top Down Parsing (TR)	Tutorial Sessions on Lex and Yacc (USNR)
05.06.2017 Monday	Bottom up Parsing - I (TR)		Bottom up Parsing - II (TR)	Error Detection and Recovery (USNR)	Tutorial on Parsing Table Generation (USNR)
06.06.2017 Tuesday	Syntax Directed Translation - I (TR)		Syntax Directed Translation - II (TR)	Intermediate Code Generation (USNR)	Testing Grammar for Subset C (USNR)
07.06.2017 Wednesday	Backpatching (USNR)		Introduction to MSIL (CHS)	MSIL Code Generation (CHS)	Lab on MSIL Code Generation - I (CHS)
08.06.2017 Thursday	Introduction to Data Flow Analysis (USNR)		Machine Independent Optimizations - I (USNR)	Machine Independent Optimizations - II (CHS)	Lab on MSIL Code Generation - II (CHS)
09.06.2017 Friday	Machine Independent Optimizations - III (CHS)		Register Allocation: Local methods, Graph colouring (CHS)	Code Scheduling: Instruction Scheduling, Speculative Scheduling, Software Pipelining (CHS)	Tutorial on Register Allocation (GSR)
10.06.2017 Saturday	Control-Flow and Low Level Optimizations (CHS)		Inter procedural Analysis (CHS)	Dependence Analysis for Parallelizing Compilers (CHS)	Tutorial on Code Scheduling, Inter procedural Analysis (GSR)
11.06.2017 Sunday	Loop Transformations for Parallelization (CHS)		Pedagogy Tools (DVLNS)	Pedagogy Tool Demo (DVLNS)	Valedictory

Faculty Development Programme (FDP) ON Advanced Compiler Design
Appendix I

Tentative Schedule

(2nd June 2017 to 11th June 2017)

RGR	Prof. R. Govindarajulu
TR	Prof. T. Ramesh, NIT Warangal
DVLNS	Prof D.V.L.N. Somayajulu, NIT Warangal
SP	Dr Suresh Purini, IIIT, Hyderabad
RU	Ramakrishna Upadrasta, IIT, Hyderabad
CHS	Dr. Chapram Sudhakar, NIT Warangal
USNR	Dr. U.S.N. Raju, NIT Warangal
GSR	Sri. G. Sreedhar Reddy, GITAM, Hyderabad

Dr.Chapram Sudhakar & Mr. G. Sreedhar Reddy, Coordinators, ACD Workshop