



No. NITW/Ad-hoc/2022/

Date: 06-12-2022

**AD-HOC FACULTY POSITIONS FOR THE ODD SEMESTER OF
ACADEMIC YEAR 2022-23**

Applications are invited for Ad-hoc Faculty positions purely on **temporary basis** in the **Department of Electrical Engineering** of NIT, Warangal.

Essential qualifications:

- First class at UG and PG Levels.
- UG Level: **BTech/BE in Electrical Engineering/ Electrical & Electronics Engineering/ Electrical power Engineering**
- PG Level: **M.Tech/ M.E. in Electrical Engineering with specialization in Power Systems Engineering, Power Electronics and Drives, Smart Electric Grid or allied Specializations**
- **Ph.D in Electrical Engineering is preferred.**

Pay package: Rs.60,000/- PM for Ph.D. holders and Rs.50,000/-PM for MTech. holders.

General information:

How to apply: Applications in the prescribed format (enclosed) with scanned copies of certificates (in a single pdf), should reach the Head of the Department, Department of Electrical Engineering, NIT Warangal **on or before 21st December, 2022** by e-mail. The e-mail address of the HOD is eee_hod@nitw.ac.in. **The currently working candidates should submit the No Objection Certificate (NOC) along with the Application OR at the time interview.**

Names of the short-listed candidates will be displayed on the Institute website by 24th December, 2022, and interview will be held on 02nd January, 2023. No separate call letter will be sent to the candidates.

The short-listed candidates are required to attend an interview **on 02nd January, 2023 in the office of the Head, Department of Electrical Engineering.** However, candidates may be considered to attend online interview on case to case basis. **No TA/DA will be paid for attending the interview.** Initially, the appointment will be made for one semester and based on the performance, it will likely be extended by one more semester. **Selected candidates are required to join immediately.**

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National Institute of Technology, Warangal
(An Institution of National Importance under MHRD, Government of India)

WARANGAL - 506 004, TELANGANA STATE – INDIA

**APPLICATION FOR THE POSITION OF AD-HOC FACULTY IN THE
DEPARTMENT OF ELECTRICAL ENGINEERING.**

APPLICATION FORMAT

Photograph

1.	Name of the Applicant					:	
2.	Father's Name					:	
3.	Date of Birth					:	
4.	Social Category						
5.	Address for Correspondence						
6.	e-Mail ID:				Mobile No:		
Course	Specialization	Institute	Year of Passing	Class	Percentage / CGPA		
SSC							
UG							
PG							
Ph.D							
7.	Teaching Experience						
8.	Industrial Experience						
9.	Number of Publications						
10.	Indicate whether GATE/NET qualified						
11.	Is NOC enclosed from Current Employer if applicable? Mention "Yes / No / NA"						

➤ **Note:** Self Attested Proof(s) must be enclosed for the above claims

Date:

SIGNATURE OF THE CANDIDATE