



राष्ट्रीय प्रौद्योगिकी संस्थान वरंगल
NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL
(शिक्षा मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)

(An Institute of National Importance under Ministry of Education, Govt. of India)

तेलंगाना / Telangana – 506004, India.

**ADVERTISEMENT FOR JUNIOR RESEARCH FELLOW (JRF) RECRUITMENT
UNDER ACCELERATED RESEARCH FOR 6G ECOSYSTEM-TTDF SCHEME/DoT**

Project title	Design and Development of an Analog Front end at 210-255GHz for 6G applications
Sponsoring agency	TTDF/DoT
Position title	JRF
Duration (in years)	Project Duration 03 years, the appointment will be for 2 Years or till closure of the project, whichever is earlier. Performance evaluation will be done each year.
Number of position(s)	One
Essential qualification	Essential: Candidates with a minimum of 60% aggregate marks or 6.5 CGPA at both UG and PG for GEN/ GEN-EWS/ OBC-NCL category and minimum 55% aggregate marks or 6.0 CGPA at both UG and PG for SC/ ST/ PwD category. OR Candidates with Bachelor's degree in Engineering/ Technology with at least CGPA of 7.5/10 or 70% of marks under GEN/ GEN-EWS/ OBC-NCL category and at least CGPA of 7.0/10 or 65% under SC/ ST/ PwD category. B.Tech/ B.E or Equivalent degree in Electronics and Communication Engineering and M.Tech/ M.E in Electronics and Communication Engineering or equivalent with Research areas in Antenna and Microwave. (OR) B.Tech/ B.E or Equivalent degree in Electronics and Communication Engineering.
Desired qualifications	Knowledge of Cadence Virtuoso/ADS software, Schematic, Layout, LVS, DRC and GDSII. Design experience in low-noise amplifiers, downconverters, phase-locked loop circuits, and system-level design will be given preference
Age limit	30 years for the GEN/ GEN-EWS category, 33 years for OBC-NCL, and 35 years for SC/ST/PwD/Female candidates.
Emoluments	Candidates who are selected through the National Eligibility Test (CSIR UGC NET or GATE) • 37,000+HRA (1st and 2nd Year) Candidate without National Eligibility Test (CSIR UGC NET or GATE) • 30,000+HRA (1st and 2nd Year) *The candidate with at least one year of JRF experience may be considered for enhancement (SRF) based on the performance
HRA/Accommodation	HRA as per the institute rules.
Job nature	Analysis, design, and simulation of low-noise amplifiers, downconverters, phase-locked loop circuits, and system-level design using Cadence Virtuoso/ADS software and 3D EM software. Characterization and testing of RF Tx and Rx ICs.

Other benefits	The selected candidates are eligible for the PhD degree at NIT Warangal, as per the institute rules. However s(he) needs to apply for a PhD position When the advertisement opens.
How to apply?	The proforma of the application is attached to this advertisement. A soft copy of the filled application with supporting documents has to be sent to the email: g.arun@nitw.ac.in, gopi.ram@nitw.ac.in
Submission mode	Please email the duly completed and signed application form, along with scanned copies of all required supporting documents, including your educational certificates, to the email addresses provided below.
Last date to apply	31/07/2026@11.59pm
Whom to contact?	Dr. Gande Arun Kumar and Dr. Gopi Ram Dept. of ECE National Institute of Technology Warangal–506004 Telangana State, India Contact no: +91 9477304602, +91 9679983382
Contact Email ID	g.arun@nitw.ac.in, gopi.ram@nitw.ac.in
Please note:	
○ The position is purely temporary ○ Please enclose a brief resume/curriculum vitae/bio-data along with the application form ○ List of eligible candidates will be announced on the institute website and intimated via email or phone. ○ Shortlisted candidate will have to attend an interview (ONLINE/OFFLINE) ○ If the interview needs to be conducted online, candidates are expected to have a good internet connection ○ No TA/DA will be paid for attending the interview	

APPLICATION FOR THE POSITION OF JRF UNDER ACCELERATED RESEARCH FOR 6 GE COSYSTEM - TTDF**SCHEME/DoT****Advt.No.NITW/2026/ECE/DoT-TTDF/TxRx/04**

(Please enclose a copy of bio-data/resume/curriculum vitae and supporting documents related to claims)

1. Name of the candidate**2. Father's name****3. Date of birth (DD/MM/YYYY)****4. Sex** Male/ Female**5. Marital status** Single/married**6. Category** OC/OBC/SC/ST**7. Address for communication****8. Mobile Number****9. Email ID****10. Educational qualifications***

Degree	Specialization/ subjects	Board/ University	Year of passing	Class	Percentage/ CGPA
SSC					
Intermediate					
B.Sc./B.E./B.Tech					
M.Sc./M.Tech.					
Ph.D.					

11. GATE/NET/ICMR/DBT-SRF score earned/ qualified*

Score	Year appeared	Score	All India rank
GATE/NET/mention			

12. Research/Industrial/Teaching experience*

S.No.	Organisation/industry served	Date/Month of entry	Date/Month of leaving	Months/Years of experience

13. Publications/conference participations***14. Any other relevant information****Declaration**

I hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. I confirm, that I am available for ONLINE interview, if necessary.

Date:

Signature of the candidate

*Submit soft copies of the supporting documents