



राष्ट्रीय प्रौद्योगिकी संस्थान वरंगल
NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL
(गिफ़्टा मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)
(An Institute of National Importance under Ministry of Education, Govt. of India)
तेलंगाना / Telangana – 506004, India.

Department of Electrical Engineering

Advertisement for JRF recruitment under a Research project

Project title	Development of a high power density medium voltage power module with integrated gate drivers and series connected soft-switched SiC MOSFETs
Sponsoring agency	Anusandhan National Research Foundation (ANRF) , Government of India
Position title	Junior Research Fellow (JRF)
Duration (in years)	3 years or till closure of the project, whichever is earlier. Performance of the research fellow will be evaluated in 3 months.
Number of position(s)	One
Essential qualification	1. Bachelor’s degree (B.Tech) in Electrical Engineering (EE), Electrical and Electronics Engineering (EEE), or Electronics and Communication Engineering (ECE). 2. Qualified in the GATE examination.

Desired qualifications	1. Knowledge of any PCB design tool, circuit simulation tools such as LTspice, and 3D modelling CAD tools. 2. Working knowledge of programming languages such as C and Python. 3.Master's degree (M.Tech) in Electrical Engineering.
Age limit	30 Years.
Age relaxation	Upper age relaxation of up to 5 years for SC/ST/OBC/Women and physically challenged candidates.
Emoluments	₹ 37,000/- + HRA per month for the first two years ₹ 42,000/- + HRA per month for the third year.
HRA/ Accommodation	HRA as per the institute rules. The selected candidate is also eligible for on-campus Hostel & Mess facilities, subject to availability.
Job nature	Electronic circuit design, Printed circuit board (PCB) design, testing and debugging of PCBs, Design of high voltage converters.
Other benefits	The selected candidate is eligible to register for a PhD degree at NIT Warangal as per the institute rules. However, s(he) needs to apply for a PhD position when the advertisement is open.
How to apply?	Complete the application (as per the proforma attached) and submit via Google Form along with the Bio-data/CV and scanned copy of certificates in a single PDF file.Google form link: https://forms.gle/jmZtMr9EHEgsrHm87 Also send a soft copy of resume, degree certificates to “mvamshi@nitw.ac.in”
Submission mode	Send a soft copy through email with the subject “ Application for JRF position ” to mvamshi@nitw.ac.in
Last date to apply	07.10.2026
Whom to contact?	Dr. Vamshi Krishna Miryala, Assistant Professor Department of Electrical Engineering National Institute of Technology Warangal – 506 004 Telangana State, India
Contact Email ID	mvamshi@nitw.ac.in

Please note:

- The position is purely temporary
- Please enclose a brief resume/ curriculum vitae/ bio-data along with the application form
- List of eligible candidates will be announced on the institute website and also intimated through email or phone
- Shortlisted candidates will have to attend an interview (ONLINE/OFFLINE)
- If interviews need to be conducted online, candidates are expected to have a good internet connection
- No TA/DA will be paid for attending the interview

APPLICATION FOR THE POSITION OF JRF
Department of Electrical Engineering
National Institute of Technology Warangal
(Please enclose a copy of bio-data/ CV and Scan copy of certificates)

1. **Name of the candidate:**
2. **Father's name:**
3. **Date of birth**
(DD/MM/YYYY):
4. **Sex:** Male/ Female/Transgender
5. **Marital status:** Single/ married
6. **Category:** GEN/GEN-EWS/OBC-NCL/SC/ST
7. **Educational qualifications:** (scan copy of original mark memos, provisional and degree certificates, GATE/NET score cards, Category certificate must be attached with this application form in pdf format)

Degree	Specialization/ subjects	Board/ University	Year of passing	Class/Division	Percentage/ CGPA
SSC					
Intermediate/ Diploma					
B.Sc./ B.E./B.Tech					
M.Sc./ M.Tech./MS					
Ph.D.					

8. GATE/ CSIR-UGC-NET/ ICMR/ UGC-NET/ DBT-JRF score earned/ qualified

Score	Year appeared	Score	All India rank
GATE/ NET/ mention			

9. Research/Industrial/Teaching experience

S.No.	Organisation/ industry served	Date/ Month of entry	Date/ Month of leaving	Months/ Years of experience

10. Publications/ conference participation

11. Any other relevant information

Declaration

I.....hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. I confirm, that I am available for ONLINE interview, if necessary.

Date:

Signature of the candidate