



(3rd to 8th June 2019)

Organized by

E & ICT Academy, National Institute of Technology, Warangal

at

MVGR College of Engineering (A), Vizianagaram, AP

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)

Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy's role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

About the Workshop:

As part of Electronics and ICT Academy initiative, a Faculty Development Programme (FDP) on ANN & Deep Learning is being conducted to the faculty members of engineering colleges, industries and research scholars and other allied disciplines from India

Major Course Contents:

- Introduction to Deep Learning.
- Hardware and Software basics for Deep Learning.
- Deep Learning Frameworks & ready-made networks CNN, RNN, DNN, GAN, RCNN.
- Image segmentation and classification techniques.
- Approaches to deal with object detection and labeling.
- Deep Learning Applications.
- Hands on session using Tensor Flow
- Research issues and case studies in Deep Learning

Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IIITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

Eligibility:

The programme is open to faculty of Engineering Colleges, MCA Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

Registration Fee Particulars:

Faculty and Research Scholars	Rs. 2500/-
Faculty of SC/ST Category	Rs. 1875/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-

SC/ST concession is only for faculty of mentioned states. Research Scholars are not eligible for SC/ST concession.

The fee is to be paid either in the form of DDs or online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of "Electronics & ICT Academy NITW " payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

Accommodation:

No TA/DA will be paid to the participants. Accommodation will be provided on prior request. Working Lunch, Tea & Snacks would be provided during the training in the campus.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with DD Receipt) should reach the coordinator by speed post. It is also mandatory to send scanned application form & DD receipt through e-mail to psr.cse@mvgrce.edu.in. The selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date (Application & DD)	25/05/2019
Selection List by E- mail	1/06/2019
Duration	3 rd to 8 th June 2019.

About NIT Warangal:

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

About MVGRCE:

MVGR was established in the year 1997 by Maharaj Alak Narayan Society for Arts and Sciences (MANSAS). MANSAS was established in 1958 by the erstwhile Raja Saheb of Vizianagaram, (Late) Dr. PVG Raju. The college has state of the art Labs which host equipment costing Rs. 13 Crore. The college is spread on a lush green 43 Acre campus and has more than 5 lakh sft. build up area. The college boasts of its library extended over 45,000 sft. area adorning an enviable collection of books. It offers Bachelor's Degree in Civil, Chemical, Computer Science, Electronics and Communication, Electrical & Electronics, Information Technology and Mechanical Engineering which were reaccredited by NBA. It also offers Post Graduate courses in Engineering and Management. CSE, Mechanical, ECE, Chemical, Civil and MBA departments are recognized as research center by JNTUK, Kakinada to offer Ph. D programs. The college is reaccredited by NAAC laboratories. Recourse persons shall be from NITs, IITs & Industries with 'A' Grade and recognized as a Business Incubator Centre by the Ministry of Micro, Small and Medium Enterprises (MSME), Govt. of India. The college is undertaking consultancy/Research projects from AICTE, DST and Govt. of AP. etc.



APPLICATION FORM

FACULTY DEVELOPMENT PROGRAMME (FDP)
ON



ARTIFICIAL NEURAL NETWORKS & DEEP LEARNING

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at

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1. Name :
2. Designation :
3. Institution :
4. Email :
5. Mobile No :
- 6.

SPONSORSHIP CERTIFICATE

Dr. /Mr. /Ms. is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "ANN & DEEP LEARNING", sponsored by Electronics & ICT Academy during 3rd to 8th June 2019 at MVGR College of Engineering (A), Vizianagaram.

DD	Online Transfer
DD No:	Reference No:
Amount:	
Bank :	
Date:	

Signature of Head of Institution
(With seal)

Address for correspondence

Post your application form with DD to

Dr. P. Srinivasa Rao,
Assoc. Professor, Dept of CSE
Maharaj Vijayram College of
Engineering(Autonomous),
Chintalavalasa, Vizianagaram-
535005
Mail id: psr.cse@mvgrce.edu.in

Mail the scanned copies of filled-
in and duly signed
Application form along with DD
to

psr.cse@mvgrce.edu.in

For more Information visit:

<http://www.nitw.ac.in/eict/>

or

www.mvgrce.edu.in/ann-deep-learning

For any enquiry contact

Coordinators:

Dr. P. Srinivasa Rao.
Mobile: 9866370352

7. Address for Correspondence:
 8. Educational Qualification:
 9. Subjects taught so far:
 - 10.No. of refresher courses/workshops attended:
 11. Experience (in years):
Teaching: Research: Industry:
 12. Accommodation required: YES /NO
 13. Do you belong to SC/ST : YES /NO
- (If yes, please specify and attach a copy of caste certificate to claim the concession)

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

Coordinators

Dr. R B V Subramanyam
Professor,
Dept. of CSE,
NIT-Warangal, Warangal, TS

Dr. P Srinivasa Rao,
Associate Professor,
Dept. of CSE,
MVGR College of Engg.(A), Vizianagaram, AP