**FACULTY DEVELOPMENT PROGRAMME (FDP) ON**

# CMOS Integrated Circuits for Instrumentation and IoT application

**(Oct 28th – Nov 2nd , 2019)**
**Organized by**
**E & ICT Academy, National Institute of Technology, Warangal. At**
**VIGNAN'S Institute of Engineering for Women**
**Kapujaggarajupeta, VSEZ (Post), Visakhapatnam, Andhra Pradesh, 530049.**
**(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)**

### Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

### About the FDP:

The vision of this FDP is to enhance the quality of education imparted in institutions by providing faculty members with a platform to learn all about **CMOS Integrated Circuits for Instrumentation and IoT application**. Educators will get to learn the best practices in these domains so that they can teach the future workforce the required skill sets.

### Major Course Contents:

- SoC-Design Verification-Trends, Challenges, Opportunities
- Recent Trends in VLSI Physical Design and Architecture for Smart IoT devices
- Secrets of VLSI Physical Design &amp; Challenges for IoT Design and IoT Using Xilinx FPGA
- Embedded systems Role of Mentor Product for Embedded Design and Challenges in Embedded IoT Design
- Low Power Design Aspects of Embedded IoT Devices
- IoT Value chain Demystified IoT Basics; future Trends, Interfacing mechanism
- Role of IoT in Smart Automation.

### Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IIITs/Central Universities/Scientific Organizations are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

### Eligibility:

The programme is open to faculty of Engineering Colleges, MCA Colleges and other allied disciplines (like Mathematics, Physics) in India. Industry personnel working in the concerned /allied discipline can also attend.

### Registration Fee Particulars:

| | |
|---|---|
| Faculty and Research Scholars | 2500/- |
| Industry Participants | 7500/- |

*The fee is to be paid either in the form of DDs or online transfer using the following details:

| DD Details | Online Transfer Details |
|---|---|
| Demand Draft in favor of "Electronics & ICT Academy NITW, NIT Warangal" payable at any bank in Warangal | Account Name: Electronics & ICT Academy NITW<br>Account No: 62423775910<br>IFSC: SBIN0020149 |

### Accommodation:

Accommodation will be provided to the selected outside participants on prior request. No TA/DA will be paid for the participants. Working Lunch, Tea & Snacks would be provided during the Training in the campus.

### How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to **sudhakar.jyo@gmail.com.**Selection will be intimated only through mail.

### Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GoI norms.

### Important dates:

| | |
|---|---|
| **Last date (Application & DD)** | **19th Oct 2019** |
| **Selection List by E- mail** | **21st Oct 2019** |
| **Duration** | **Oct 28th – 2nd Nov -19** |

### About NIT Warangal:

**National Institute of Technology, Warangal** is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and PhD programs in various specializations. All B. Techand M. Tech programmes of NIT Warangal are NBA accredited.

### About VIEW:

Department of Electronics and communication Engineering (ECE) at VIGNAN'S institute of engineering for women was established in the year 2008 for imparting state-of-the-art knowledge in Electronics and Communication Engineering and allied areas. Students who are eligible students are placed on campus and many of them are placed in different MNC companies every year. The department has set up laboratories with excellent infrastructure in the areas of Electronics, Communication, VLSI, and Embedded. Faculties are actively involved in R&D activities and are working on research projects funded by agencies like DST.

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1. Name :

2. Designation :

3. Institution :

4. Email :

5. MobileNo :

6.

| DD | Online Transfer |
|---|---|
| DD No: | Reference No: |
| Amount: | |
| Bank : | |
| Date: | |

7. Address for Correspondence:

8. EducationalQualification:

9. Subjects taught sofar:

10. No. of refresher courses/workshopsattended:

11. Experience (inyears):

    Teaching:    Research:    Industry:

12. Accommodationrequired:    YES /NO

13. Do you belong toSC/ST :    SC ☐    ST ☐

**Declaration**

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

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**SPONSORSHIP CERTIFICATE**

Dr. /Mr. /Ms. …………………………….. is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **"CMOS Integrated Circuits for Instrumentation and IoT application"**, sponsored by Electronics & ICT Academy during Oct 28**th – 2nd Nov, 2019**, at **VIGNAN'S Institute of Engineering for women** kapujaggarajupeta,visakhapatnam.

Signature of Head ofInstitution
(withseal)

**Address for correspondence**
*Post your application form with DD to*

Head, Department of ECE
VIGNAN'S Institute of Engineering for Women
**Kapujaggarajupeta, VSEZ (Post), Visakhapatnam, Andhra Pradesh-530049**, India.

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*E-mail the scanned copies of filled-in and duly signed application form along with DDto*

**E-Mail-ID: sudhakar.jyo@gmail.com**

*For more details about Electronics & ICT Academy, NIT, Warangal, please visit:*
*https://nitw.ac.in/eict*

*For more enquiries please contact:*

***Mobile:***

**Mr. K. Rajendra Prasad: 7093108793**

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## Coordinators

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