



FACULTY DEVELOPMENT PROGRAMME (FDP) ON NANOSCALE DEVICES AND CIRCUITS

(17th – 22nd June, 2019)

Organized by

E & ICT Academy, National Institute of Technology, Warangal.

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)



Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, Gol. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

About the FDP:

As part of this initiative, a Faculty Development Programme (FDP) on Nanoscale devices and circuits is being conducted at National Institute of Technology Warangal, Warangal, Telangana to the Faculty members of Engineering, MCA and other allied degree colleges.

Major Course Contents:

- Introduction to Nano-scale MOS modeling
- Nanowire, Multigate (MuG), and tunneling based MOS devices
- Nano-scale Interconnects: modeling the parasitics
- Post CMOS devices and interconnects based on CNT's, GNR's and spintronics
- Integration methodologies : TSVs, 3D, interconnects, NEMS
- Spintronic devices and non-volatile logic
- Basic illustration of SPICE with examples
- SRAM Design on SPICE
- Basic Illustration of TCAD with examples
- TCAD Simulation of advanced nanoscale devices (Diode, MOSFET, 2D FinFET)
- Modeling and simulation of advanced devices based on CNTs, GNR's
- Spintronic device simulation using OOMMF

Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IIITs/Industry are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

Eligibility:

The programme is open to faculty of Engineering Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

Registration Fee Particulars:

Faculty and Research Scholars	Rs.2500/-
Faculty of SC/ST Category	Rs.1875/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs.7500/-

SC/ST concession is only for faculty of mentioned states. Research Scholars are not eligible for SC/ST concession.

The entire registration fee is to be collected in the form of DDs/online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of "E & ICT Academy, NIT Warangal" payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

Venue: Seminar Hall & ECE Labs, NITW.

Accommodation:

Accommodation will be provided to the selected outside participants on prior request. No TA/DA will be paid for the participants. Working Lunch, Tea & Snacks would be provided during the Training in the campus.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with payment receipt) should reach the coordinator by speed-post. It is also mandatory to fill the following google form and selection will be intimated only through mail.

<https://goo.gl/forms/p2hTsxYU50657Tnt2>

Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date (Application & Online)	8 th June, 2019
Selection List by E- mail	9 th June, 2019
Duration	17 th – 22 nd June, 2019

About NIT Warangal:

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

The Department of Electronics and Communication Engineering offers B.Tech. course in ECE, M.Tech. courses in Advanced Communication Systems (ACS), VLSI Design and Electronic Instrumentation. The Department has experienced faculty with good publications and well-established laboratories. The Department has liaison with reputed industries and R&D Organizations.



APPLICATION FORM
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ON
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SPONSORSHIP CERTIFICATE

1. Name : Dr. /Mr. /Ms. is an
2. Designation : employee of our Institute/Organization and is hereby
3. Institution : sponsored to participate in the FDP on “**Nanoscale
Devices and circuits**”, sponsored by Electronics &
4. Email : ICT Academy during 17th – 22nd June, 2019 at
5. Mobile No : National Institute of Technology Warangal.
6.

DD	Online Transfer
DD No:	Reference No:
Amount:	
Bank :	
Date:	

Signature of Head of Institution
(with seal)

Address for correspondence

Post your application form with payment proof to

Dr. Shivam Verma,
Assistant Professor,
Room No. ECE307, Department of ECE,
National Institute of Technology Warangal.
Warangal-506004

7. Address for Correspondence:

Fill the following google form with scanned copies of filled-in and duly signed application form along with payment proof as attachment.

<https://goo.gl/forms/p2hTsxyU50657Tnt2>

8. Educational Qualification:

9. Subjects taught so far:

10. No. of refresher courses/workshops attended:

For more details about Electronics & ICT Academy, NIT, Warangal, please visit:

<https://nitw.ac.in/eict>

11. Experience (in years):

Teaching:

Research:

Industry:

For more enquiries please contact:

Mobile: 7014681827, 9760018986

12. Accommodation required: YES /NO

13. Do you belong to SC/ST : YES /NO

(If yes, please specify and attach a copy of caste certificate to claim the concession)

Email : shivaver@nitw.ac.in,

satishm@nitw.ac.in

Note: Please carry your personal laptops.

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

Coordinators

Dr. Shivam Verma
Department of ECE
NATIONAL INSTITUTE OF TECHNOLOGY
WARANGAL – 506 004 (Telangana State)

Dr. Maheshwaram Satish
Department of ECE,
NATIONAL INSTITUTE OF TECHNOLOGY
WARANGAL – 506 004 (Telangana State)