



FACULTY DEVELOPMENT PROGRAMME (FDP) On

Signal and Image Processing

(3rd June to 8th June 2019)

Organized by

E & ICT Academy, NIT Warangal

at

Holy Mary Institute of Technology & Science, Hyderabad, Telangana



Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GOI. Role of the academy is to offer faculty development programmes in standard courses and emerging areas of Electronics, Information Communication Technologies, Training & Consultancy Services for Industry, Curriculum development for Industry, CEP for working professionals, advice and support for technical incubation and entrepreneurial activities.

A Faculty Development Programme (FDP) on "Signal and Image Processing" is being organized by the E & ICT Academy at Holy Mary Institute of Technology & Science, Hyderabad, which has agreed to host the same in its campus by bringing and caring the participants to this programme and also extending its infrastructure for organizing the course involving actively their faculty of ECE Department.

Major Course Contents:

- Statistical Signal Processing
- Communication Signal Processing
- Medical Image and Signal Analysis
- Multidimensional Signal Processing
- Character, Graphical and Text Processing
- Morphological Processing
- Document and Handwriting Analysis
- Image and Video Based Gesture Analysis
- Image Compression and Coding
- Biometrics and Pattern Recognition
- Feature Selection and Extraction
- Real-Time Issues in Imaging
- Signal and Image Processing Applications in Remote Sensing.

Resource persons:

E & ICT Academy takes the responsibility of organizing lectures by academicians from reputed institutes in India.

Eligibility:

The programme is open to the faculty of engineering colleges in India. Industry personnel working in the concerned/allied discipline can also attend.

How to Apply: A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with DD/NEFT Receipt) should reach the coordinator by speed post. It is also mandatory to send scanned application form and DD/NEFT receipt through e-mail as selection will be intimated only through mail.

Registration Fee Particulars:

Faculty and Research Scholars	Rs. 2500/-
Faculty of SC/ST Category	Rs. 1875/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-

SC/ST concession is only for faculty of mentioned states. Research Scholars are not eligible for SC/ST concession.

The fee is to be paid either in the form of DDs or online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of " Electronics & ICT Academy NITW " payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

Accommodation: Free Accommodation/ boarding will be provided for the all participants by the host Institute. No TA/DA will be paid to the participants.

Selection Criteria: Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified immediately. The maximum number of participants will be 50(Fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified in the institute website www.nitw.ac.in and also will be sent to their personal e-mail ids. In case a candidate is not selected, the demand draft will be returned. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with test. Reservations are followed for selecting candidates as per GOI norms.

Important Dates:

Last date for submission of application: **27th May 2019**

Selection-list intimation/display before : **1st June 2019**

Duration of the Program : **3rd June 2019 to 8th June 2019**

About NIT, Warangal:

National Institute of Technology (formerly Regional Engineering College), Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institution imparting technical education of a very high standard leading to the B. Tech., degrees in various branches of engineering, M. Tech. and Ph. D programs in various specializations.

About Institute of Aeronautical Engineering:

Holy Mary Institute of Technology & Science was established in the year 2001 by the Holy Trinity Educational Society at Bogaram (V), Keesara (M), Medchal District. It is promoted by the well known Educationalist and Philanthropists Dr. Arimanda Vara Prasad Reddy, Honorable Chairman, Smt Dr. A. Vijaya Sarada Reddy, Honorable Secretary and Sri A. Siddhartha Reddy, Honourable Vice Chairman. Dr. P. Bhaskara Reddy is the Principal/Director. The College of Engineering has well equipped laboratories, library, fully furnished class rooms, staff rooms, dedicated faculty and other facilities like English Language lab and Internet lab with high capacity leased line for Internet. An exclusive office named International Students Cell (ISC) is developed for admissions and guidance of International students. Holy Mary is partnered with International Accreditation Council of Quality Education and Research (IACQER), an organization that encourages in R & D activities. The College is approved by the All India Council for Technical Education, New Delhi and Accredited by NAAC-'A' Grade and is permanently affiliated to JNTU, Hyderabad.



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APPLICATION FORM

1. Name of the Applicant :

2. Designation :

3. Institution :

4. E-mail ID :

5. Mobile :

6. Category :

7. Payment Mode

☐ Faculty/Scholars from mentioned states (Rs. 2500/-)

Demand Draft No: _____

☐ Industry Participants (Rs. 7500/-)

NEFT / UTR No.: _____

☐ Participants from other states (Rs.2500/-)

☐ SC/ST Participants (Rs. 1875/-)

Bank: _____ Date: _____

8. Address for Correspondence:

9. Subjects taught so far:

10. Educational Qualification with Specialization:

11. No. of refresher courses / workshops attended:

12. Experience (in Years) :

Teaching:

Research:

Industry:

13. Do you belong to reservations SC/ST(**YES/NO**):If YES, please specify (**Attach photo copy to claim concession)

14. Do you require accommodation during Training : **YES/NO**

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place:

Date:

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms.....is an employee of our Institute/Organization and is here by sponsored to participate in the FDP on Signal and Image Processing sponsored by **Electronics & ICT Academy** during 3rd June to 8th June 2019 at **Holy Mary Institute of Technology & Science, Hyderabad**.

Place:

Date:

Signature of Principal/Head of Institution

Post your application form along with DD/NEFT Receipt to:

Dr. K. Kanthi Kumar, Assoc. Professor, Dept. of ECE,
Holy Mary Institute of Technology & Science,
Email: hitsece@hmg.ac.in or kanthikumar.kampa@hmg.ac.in,
Mobile: 9618111744, 9949311310

Dr. Srihari Rao Patri
Coordinator, FDP on RACS,
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For more details about Electronics and ICT academy please visit: <http://nitw.ac.in/eict/>