



## FACULTY DEVELOPMENT PROGRAMME (FDP) ON

# VLSI & FPGA SIGNAL PROCESSING

(2<sup>nd</sup> – 7<sup>th</sup> May, 2018)

Organized by

**GITAM Institute of Technology, GITAM (Deemed to be University)**

in association with

**E & ICT Academy, National Institute of Technology, Warangal.**

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)



### Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

### About the Workshop:

The goal FDP on above topic is for the Faculty and scientists currently doing research and development in field of VLSI and related signal processing. The physics based modeling of various semiconductor devices is an important area of research for the growth and development of semiconductor science and technology. While analytical modeling gives the physical inside of the device characteristics, TCAD/ TSUPREMO simulations can provide the first hand information of various advanced semiconductor devices (DG MOS FET/FinFET/TFET) without going through complex mathematical modeling and practical fabrication and experimentation. The course will also cover MEMS and VLSI Inter connection.

### Major Course Contents:

- Signal processing Algorithms.
- Review of microelectronics.
- Low power Design Techniques
- Device physic.
- Numerical analysis/Methods of circuit analysis.
- Device characterization.
- Analog Design (Frontend-1: components available-active & Passive)
- Analog Design (Frontend-II: Transistor sizing)
- Analog IC Design (Physical Design-Issues & Challenges)
- Design kit (How to use)
- Chip Finishing: Packaging, testing, etc.
- Nano technology in bio medical & bio sensing applications
- CADANCE flow
- Introduction to TCAD device simulators

### Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IIITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

### Eligibility:

The programme is open to faculty of Engineering Colleges, MCA Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

### Registration Fee Particulars:

Faculty and Research Scholars	Rs. 2500/-
Faculty of SC/ST Category	Rs. 1250/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-

**SC/ST concession is only for faculty of mentioned states.  
Research Scholars are not eligible for SC/ST concession.**

The fee is to be paid either in the form of DDs or online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of "Director, NIT Warangal" payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

**Venue:** ICT Bhavan, GIT, GITAM University, Visakhapatnam.

### Accommodation:

Lodging and boarding will be provided at GITAM University campus with nominal cost.

### How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to [hod\\_ece@gitam.edu](mailto:hod_ece@gitam.edu) as selection will be intimated only through mail.

### Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

### Important dates:

Last date (Application & DD)	25 <sup>th</sup> April, 2018
Selection List by E- mail	27 <sup>th</sup> April, 2018
Duration	2 <sup>nd</sup> – 7 <sup>th</sup> May, 2018

### About NIT Warangal:

**National Institute of Technology, Warangal** is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

### About GITAM:

During the past 37 years, GITAM has been committed to excellence in higher education foraying into diversified disciplines of learning. It has steadily evolved into a world class university recognized for the experiential learning it offers, the competent and compassionate faculty, the stellar research laboratories, academic vibrancy and cosmopolitan culture. Dr.M.V.V.S.Murthi, visionary, industrialist, educationist, philanthropist and former parliamentarian has been guiding the destiny of the University since its inception. The university has three campuses - its main campus at Visakhapatnam, off campuses at Hyderabad and Bangalore. Known for its creative dynamism and flexibility, the university offering 106 undergraduates, postgraduate and doctoral programmes in Engineering, Science, International Business and management are blending skill development and value orientation to shape the career of students and develop holistic personality to be privileged members of the civil society. The university also offers several programmes through distance learning to increase access to higher education with emphasis on reaching the unreached.



## APPLICATION FORM

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1. Name :
2. Designation :
3. Institution :
4. Email :
5. Mobile No :
- 6.

#### SPONSORSHIP CERTIFICATE

Dr. /Mr. /Ms. .... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on “VLSI & FPGA Signal Processing”, sponsored by Electronics & ICT Academy during 2<sup>nd</sup> – 7<sup>th</sup> May, 2018 at GITAM Vishakhapatnam.

DD	Online Transfer
DD No:	Reference No:
Amount:	
Bank :	
Date:	

Signature of Head of Institution  
(with seal)

#### Address for correspondence

Post your application form with DD to

7. Address for Correspondence:

**Dr.Sreenivasa Rao Ijjada**  
**Department of ECE, GIT,**  
**GITAM (Deemed to be University),**  
**Rushikonda, Visakhapatnam -530045.**

8. Educational Qualification:

E-mail the scanned copies of filled-in and duly signed application form along with DD to

9. Subjects taught so far:

[jrk.nitw@nitw.ac.in](mailto:jrk.nitw@nitw.ac.in), [srinivasanaidu.ijjada@gitam.edu](mailto:srinivasanaidu.ijjada@gitam.edu)

10.No. of refresher courses/workshops attended:

For more details about Electronics & ICT Academy, NIT, Warangal, please visit:

11. Experience (in years):

<https://nitw.ac.in/eict>

Teaching:      Research:      Industry:

For more enquiries please contact:

12. Accommodation required:      YES /NO

**Mobile: +918332969363/ +919849245649/ +919989893955**

13. Do you belong to SC/ST :      YES /NO

(If yes, please specify and attach a copy of caste certificate to claim the concession)

#### Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

### Coordinators

**Dr. J. Ravi Kumar**  
**Department of ECE,**  
**NATIONAL INSTITUTE OF TECHNOLOGY**  
**WARANGAL – 506 004 (Telangana State)**

**Dr.Sreenivasa Rao Ijjada**  
**Department of ECE, GIT,**  
**GITAM (Deemed to be University),**  
**Rushikonda, Visakhapatnam -530045.**