

Applications are invited for the temporary position(s) as per the following details.

Title of the project:

“Special Man power Development Programme for Chip to Systems Design”

Funding agency: Ministry of Electronics and Information Technology (MeiTY)

Duration:

Initially for a period of one year and likely to be extended till the completion of the project.

Sl.No	Post	Qualification	Duration	Consolidated Salary (PM)
1	Guest /Temporary Faculty	ME/MTech/PhD in the field of Microelectronics/VLSI First class (60%) or 6.5 CGPA	Initially for period of one year which may be extended till the end of the project	Rs 40000/= (per month)
2	Lab Engineer	M.Tech/ M.E in microelectronics/ VLSI (First class is desirable) with knowledge of Cadence/Synopsys/Mentorgraphics/ TCAD and Linux .	As above	Rs 34000/= (per month)
3.	Project assistant	B. Tech /B.E. degree in Electronics & Communication Engineering (First class is desirable) Knowledge of CAD tools and Linux Programing is preferable	As above	Rs 20, 000/- per month

Applications with full bio-data and copies of supporting documents should reach the Dr P Sreehari Rao, Associate Professor, ECE Department, NIT Warangal -506004 on or before 4thMay 2018. A soft copy of the application may be mailed to patri@nitw.ac.in. Names of short listed candidates called for interview will be displayed in the Institute website: www.nitw.ac.in and they have to attend the interview at their own cost.