



NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL
(An Institution of National Importance)
WARANGAL- 506 004, Telangana State, India
Department of Electrical Engineering

Adv.No EED/SPS/SERB-CRG/2019/ 3037

Date: 03/07/2019

Advertisement for the position of Junior Research Fellow

Applications are invited from interested candidates for one temporary position of **Junior Research Fellow** (JRF) to work in the ongoing research project sponsored by Department of Science and Technology - Science and Engineering Research Board (**DST-SERB**), Govt. of India.

Details of qualifications, Emoluments and duration are given below:

Project Title	<i>Development of High PF Grid connected Induction Cooker with Direct AC-AC Conversion for Vessels of Different Material</i>
Name of Position	Junior Research Fellow (JRF)
Eligibility	First class with not less than 60% aggregate (or) not less than 6.5 CGPA on a 10 point scale in B. Tech./B.E. in Electrical Engineering. and First class with not less than 60% aggregate (or) not less than 6.5 CGPA on a 10 point scale in M.Tech in Power Electronics and Drives. The candidate must have M.Tech through GATE (or) a valid GATE score. Exposure to fabrication & testing of power electronics circuits is desirable.
Duration	3 Years
Consolidated Salary	31,000/- pm for first two years and 35,000/- pm for third year. HRA as per institute rules. The selected candidate is also eligible for on campus Hostel & Mess facilities at their own expenses.
Other Benefits	The selected candidate is eligible to register for Ph.D degree at NIT, Warangal as per institute rules.

How to Apply:

Interested and eligible candidates are required to apply in the prescribed application format available on the website www.nitw.ac.in. Complete application with the supporting copies of certificates should be sent as hard copy by post to the address mentioned below with a superscription on the envelope "APPLICATION FOR THE POST OF JUNIOR RESEARCH FELLOW (DST-SERB)" and a softcopy of application with the supporting documents should be sent by email to selvi@nitw.ac.in so as to reach by **05/08/2019**.

Selection Procedure:

The candidates will be selected through written test and/or interview. List of eligible candidates and date of written test / interview will be announced through the institute website. No TA/DA will be paid for attending the interview.

NOTE:

Candidates who are already employed should produce "No objection Certificate" (NOC) from the employer and students should provide provisional degree certificate or its equivalent at the time of interview. The selected candidate is expected to produce a Relieving Certificate from their employer or provisional degree certificate in case of students and join the duties within a week after intimation of their selection else it would be offered to a candidate in the waiting list.

Application should be sent to:

Dr. S Porpandiselvi
Principal Investigator (DST-SERB-CRG Project)
Assistant Professor
Department of Electrical Engineering
National Institute of Technology Warangal
Warangal - 506004, Telangana State, India.

Email id: selvi@nitw.ac.in

Phone: +91 8332969288

Application for the Position of JRF

DST-SERB Sponsored Project

“Development of High PF Grid connected Induction Cooker with Direct AC-AC Conversion for Vessels of Different Material”

Last Date to Receive the filled-in Application : 05/08/2019.

1. Name in full (In BLOCK LETTERS) :

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2. Date of Birth (DOB) :

3. Sex (M/F):

4. Marital Status (Single/Married):

5. Nationality:

6. Phone No:

7. Mobile No:

8. Email address:

9. Address:

Correspondence:

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Permanent:

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10. Qualifying Degree: (matriculation/ 10th onward, attach the copy of proof)

Examination (Mention specialization if applicable)	Name of the institution	Name of Board/University	Year of Passing	Percentage of Marks/ CGPA out of 10

11. GATE details: (Enclose the copy of GATE score card)

Appeared Year:

GATE RANK:

GATE score:

12. Details of Experience if any: (Enclose the copy of experience certificate and/or NOC)

13. Publications if any (Enclose first page of publication):

DECLARATION

I hereby declare that all statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled.

Date/Place:

Signature