



The following candidates are shortlisted for written test/interview for Junior Research Fellow (JRF) to work in the Science and Engineering Research Board (SERB) sponsored project entitled “**Design and Development of High-Efficient PFC based AC-DC LED Driver for AC-Grid Fed LED Lighting System**” (Ref. No. EMR/2017/004913)”, sanctioned by Department of Science and Technology, Government of India, in the Electrical Engineering Department, NIT Warangal.

**List of Short Listed Candidates:**

| S.No. | Name of the candidate  |
|-------|------------------------|
| 1.    | L. Harsha Vardhan      |
| 2.    | B. V. S. S. S. Gopal   |
| 3.    | Vinay Kumar Singh      |
| 4.    | Prabhat Kumar Singh    |
| 5.    | Dodda Satish Rteddy    |
| 6.    | Ambati Dinesh Kumar    |
| 7.    | Sourav Prasad          |
| 8.    | Sagar Petkar           |
| 9.    | Mohammad Mukheem Basha |
| 10.   | Anurag Pratap Singh    |

**Date of interview: 12-December-2018, Wednesday**

**Instructions to the candidates:**

- The above candidates are requested to attend written test/interview scheduled on **12.12.2018 at 10 AM** along with **all original certificates**.
- They are requested to report to the Principal Investigator in the Department of Electrical Engineering on **12.12.2018 at 9:30 AM** for scrutiny of certificates.
- Candidates are to be noted that there is no separate call letter for the interview.
- **Candidates are advised to inform their participation in the interview on or before 10.12.2018 by 5:00 PM through email to the Principal Investigator.**
- No TA/DA and no accommodation will be provided for attending the interview.
- Candidates are advised to make arrangements for their stay for an additional day, if required.

For any other details, you may contact at the following address between **9 am to 5 pm**.

**Dr. B. L. Narasimharaju,**  
Associate Professor & Principal Investigator  
Department of Electrical Engineering,  
National Institute of Technology Warangal, Telangana State.  
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