



NATIONAL INSTITUTE OF TECHNOLOGY :: WARANGAL
WARANGAL – 506004, Telangana, INDIA
Department of Electrical Engineering

No.: NITW/EED/BLNR/SERB/ADVT/2018/01.

Dated: 07 August, 2018

Advertisement for the Position of Junior Research Fellow (JRF)

Applications are invited from the interested candidates for **ONE** post of **Junior Research Fellow (JRF)** under Science and Engineering Research Board (SERB), India, sponsored research project entitled **“Design and Development of High-Efficient PFC based AC-DC LED Driver for AC-Grid Fed LED Lighting System” (Ref. No. EMR/2017/004913)**. The staff will be appointed initially for one year (contractual) and the services may be extended till the end of the project based on the performance review.

Details of post, Qualifications, Emoluments and Duration etc..:

Name of the Position	Junior Research Fellow (JRF) (purely on Temporary basis)
Number of posts	01 – Post
Eligibility	First class with not less than 60% aggregate (or) not less than 6.5 CGPA on a 10 point scale in B.Tech/BE in Electrical Engineering and First class with not less than 60% aggregate (or) not less than 6.5 CGPA on a 10 point scale in M.Tech/ ME in the specializations of Power Electronics (or) Power Electronics and Drives (or) Power Electronics and Power Systems from a recognized technical institute or university as a full time program. The candidate must have M.Tech through GATE (or) a valid GATE score. Those who are appearing for the M.Tech/M.E final semester examination may also apply. Proof of M.Tech/M.E certificate has to be provided during the time of interview.
Duration	Total Three years, but initial appointment for a period of one year and may be extended up to the period of project duration based on annual performance review.
Consolidated Salary	1. Rs.25,000/- pm for first 2-years and Rs.28,000/- pm for third year. 2. HRA as approved by SERB and institute norms as applicable. 3. Selected candidate is eligible for on campus Hostel & Mess facilities at their own expenses.
Other Benefits	Selected candidate is eligible to register for PhD degree at NIT, Warangal.
Age Limit:	Preferably less than 30 years as on the last date for submission of application. However, age relaxation will be given for the deserving candidates.

Desired Skills:-

- Strong fundamentals in electrical engineering subjects including power electronics.
- Knowledge of simulation software such as PSIM, MATLAB etc.
- Experience of working on implementation of hardware power converter modules/PCB design
- Basic knowledge of VHDL/ DSP programming or equivalent to generate control signals.
- Experience in on PFC converters & Hardware fabrication
- Ability to write technical research papers, work in a team and good communication skills.
- Working knowledge of LED drivers for lighting systems is most desirable.

How to apply: Interested and eligible candidates are required to apply in the prescribed application format as given below. **Hard copy of filled application as per given application format and all the supporting copies of certificates should be sent by post to the address mentioned below** with a superscription on the envelope containing application as “**Application for the Post of Junior Research Fellow (SERB)-BLNR**” so as to reach by **27th August 2018**, and also, a softcopy of application along with the scanned copies of supporting documents should be email to the principal investigator email ID: narasimharaju.bl@gmail.com.

Attention:

- The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
- The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
- Mere possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
- Shortlisted Candidates will be informed by email (apart from website) about the interview date. The candidates must provide valid E-mail IDs in the application.
- Shortlisted candidates have to present themselves for the interview on the interview date with updated CV, and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
- Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” (NOC) from the employer at the time of interview.
- No TA/DA will be paid for appearing in the interview.
- Last date for receiving the duly filled in application is **27th August 2018**.
- Intimation regarding written test/interview will be posted in the Institute website.

ADDRESS FOR CORRESPONDENCE:

Dr. B. L Narasimharaju,

Associate Professor & Principal Investigator

Department of Electrical Engineering,

National Institute of Technology Warangal,

Warangal-506004, Telangana State.

Ph: +91-8332969294; **Email:** narasimharaju.bl@gmail.com



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Application for the Position of JRF

Under SERB Project

Design and Development of High-Efficient PFC based AC-DC LED Driver for AC-Grid Fed LED Lighting System

1. Name of the Candidate (BLOCKLETTER):
2. Father's Name (BLOCKLETTER):
3. Mother's Name (BLOCKLETTER):
4. (a) Date of Birth: (DD/MM/YYYY) _____
(b) Sex (Male/Female/Other): _____
(c) Marital Status: Married/Single:
(d) Nationality:
(d) Category: SC/ST/OBC/PWD/General
5. Details of Experience if any (attach copy of experience certificate and/or NOC):
6. NET/GATE: Qualified (Yes/No): Score: _____ Rank: _____
Specialization: _____ Year: _____
7. Academic Qualification: (Starting from Standard 10 or equivalent Examination):

**Affix signed
passport size
recent
photograph**

Name of Exam Passed	Name of the School/College/Institute/University	Year of Passing	Discipline/Specialization	Percentage of Marks/ CGPA out of 10

8. Phone Numbers: Landline with STD code: _____ Mobile No :

9. Email ID:

10. Address

(i) Correspondence:

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(ii) Permanent:

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11. Contact Details of two referees:

Details	Referee I	Referee II
Name:		
Designation:		
Organization:		
Office Address:		
Phone Numbers:		
Email ID:		

12. Publications if any (attach copies of best THREE papers in your opinion):

13. For any other relevant information:

DECLARATION

I hereby declare that all the information furnished in this application is true to the best of my knowledge and belief. In the event of the any information being found false/incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled. If selected, I promise to abide by the rules and regulations of the Institute.

Date & Place:

Signature of the Candidate

Note: If any extra information is to be furnished, separate sheets may be attached.