



# NATIONAL INSTITUTE OF TECHNOLOGY

WARANGAL-506004  
DEPARTMENT ECE

**Applications are invited for the temporary position as per the following details**

Title of the project:

**Special Manpower Development Programme for Chip to System Design**

Funding agency: Ministry of Electronics and Information Technology (MeiTY)

Duration:

Initially for a period of six months and likely to be extended till the completion of the project

Sl No	Post	Qualification	Duration	Consolidated salary(p.m.)
1	Lab Engineer (1)	M.Tech/ME in micro electronics/VLSI (First class is desirable) with knowledge of Cadence/synopsys/mentorgraphics/TCAD and Linux	Initially for period of six months which may be extended till the end of the project	Rs 34,000/- (per month)

Applications with full bio-data and copies of supporting documents should reach the Dr.P. Sreehari Rao, Associate professor, ECE department, NIT Warangal – 506004 on or before 10<sup>th</sup> June 2019. A soft copy of the application may be mailed to [patri@nitw.ac.in](mailto:patri@nitw.ac.in). Names of shortlisted candidates called for interview will be displayed in the Institute website: [www.nitw.ac.in](http://www.nitw.ac.in) and they have to attend the interview at their own cost.